

# **CR05AM-16A**

800V-0.3A-Thyristor Low Power Use

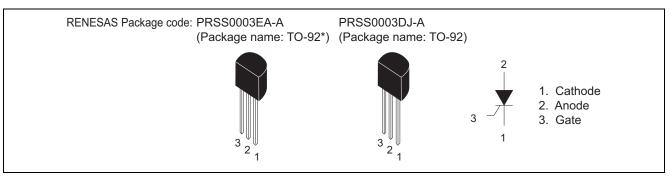
#### Features

- $I_{T(AV)}: 0.3 A$
- V<sub>DRM</sub> : 800 V
- I<sub>GT</sub> : 100 μA
- RoHS Compliant

## Non-Insulated Type

- Planar Passivation Type
- Halogen-free package (PRSS0003DJ-A)
- Completely Pb-free package (PRSS0003DJ-A)

### Outline



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### Applications

Leakage protector, timer, and gas igniter

#### **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit
Falameter	Symbol	16	– Unit
Repetitive peak reverse voltage	V <sub>RRM</sub>	800	V
Non-repetitive peak reverse voltage	V <sub>RSM</sub>	960	V
DC reverse voltage	V <sub>R(DC)</sub>	640	V
Repetitive peak off-state voltage Note1	V <sub>DRM</sub>	800	V
Non-repetitive peak off-state voltage Note1	Vdsm	960	V
DC off-state voltage Note1	V <sub>D(DC)</sub>	640	V

Notes: 1. With gate to cathode resistance  $R_{GK} = 1 \ k\Omega$ 

R07DS0988EJ0200

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Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T(RMS)</sub>	0.47	А	
Average on-state current	I <sub>T(AV)</sub>	0.3	A	Commercial frequency, sine half wave $180^{\circ}$ conduction, Ta = $62^{\circ}$ C
Surge on-state current	Ітѕм	10	A	60 Hz sine half wave, 1 full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.4	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P <sub>GM</sub>	0.5	W	
Average gate power dissipation	P <sub>G(AV)</sub>	0.1	W	
Peak gate forward voltage	V <sub>FGM</sub>	6	V	
Peak gate reverse voltage	V <sub>RGM</sub>	6	V	
Peak gate forward current	I <sub>FGM</sub>	0.3	A	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.23	g	Typical value



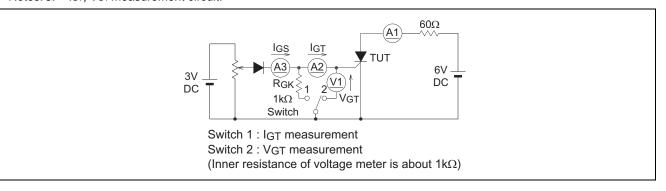
### **Electrical Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	IRRM	—	—	0.1	mA	Tj = 125°C, V <sub>RRM</sub> applied
Repetitive peak off-state current	I <sub>DRM</sub>	—	—	0.1	mA	Tj = 125°C, V <sub>DRM</sub> applied R <sub>GK</sub> = 1 k $\Omega$
On-state voltage	V <sub>TM</sub>	—	—	1.8	V	Tj = 25°C, $I_{TM}$ = 4 A instantaneous value
Gate trigger voltage	V <sub>GT</sub>	—	—	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A^{Note2}$
Gate non-trigger voltage	V <sub>GD</sub>	0.2	_	_	V	$Tj = 125^{\circ}C, V_D = 1/2 V_{DRM}$ R <sub>GK</sub> = 1 kΩ
Gate trigger current	I <sub>GT</sub>	1 Note2	_	100 Note2	$\mu A$	$Tj = 25^{\circ}C, V_D = 6 V, I_T = 0.1 A^{Note2}$
Holding current	IH	—		3	mA	$Tj = 25^{\circ}C$ , $V_D = 12 V$ , $R_{GK} = 1 k\Omega$
Thermal resistance	R <sub>th(j-a)</sub>	—	—	180	°C/W	Junction to ambient

Notes: 2. If special values of I<sub>GT</sub> are required, choose item D or E from those listed in the table below if possible.

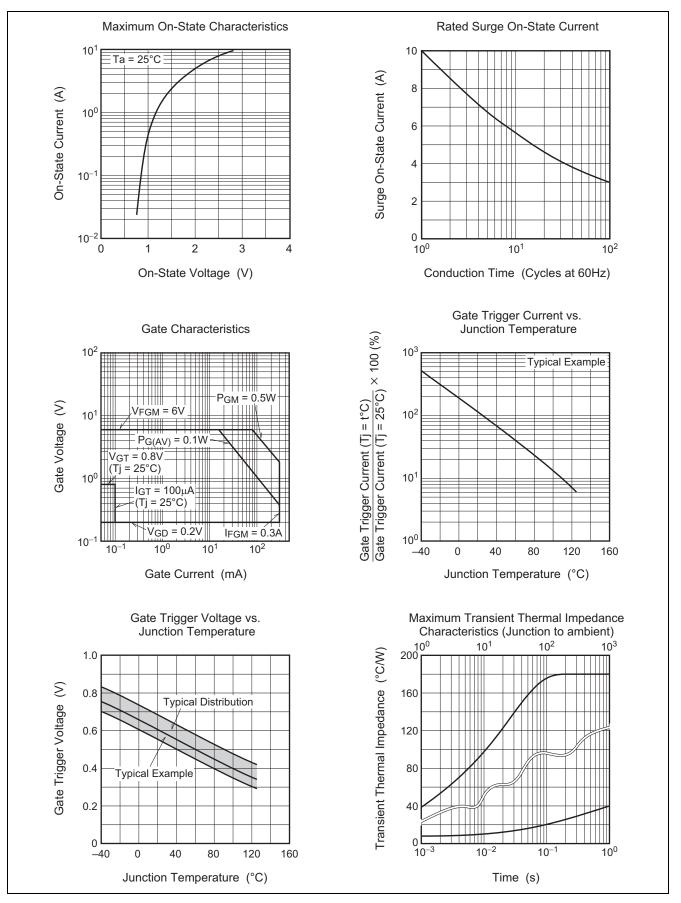
ltem	D	E
I <sub>GT</sub> (μΑ)	1 to 50	20 to 100

The above values do not include the current flowing through the 1 k $\Omega$  resistance between the gate and cathode. Notes: 3. IGT, VGT measurement circuit.

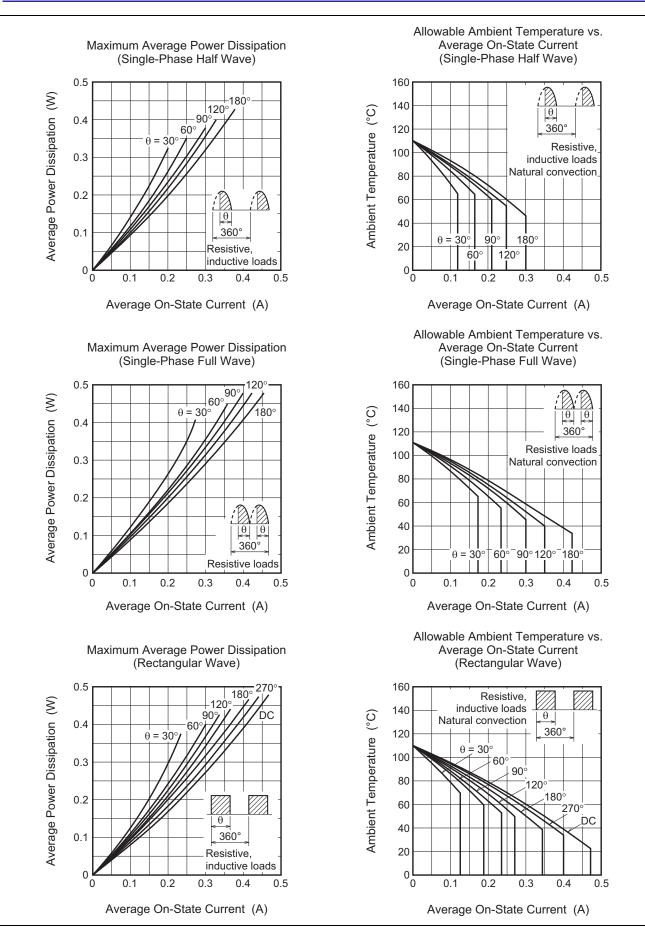


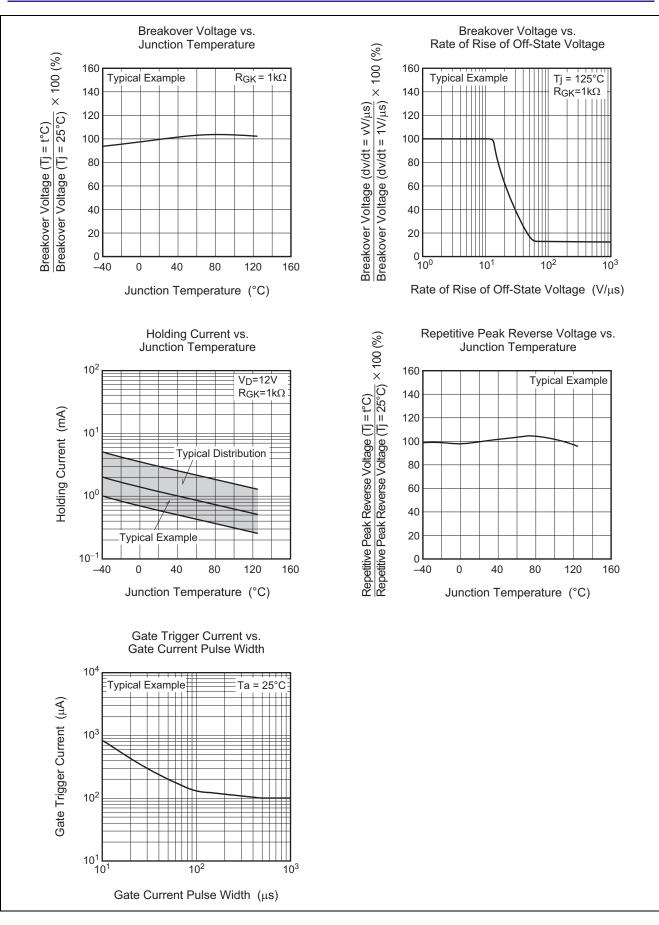


#### **Performance Curves**



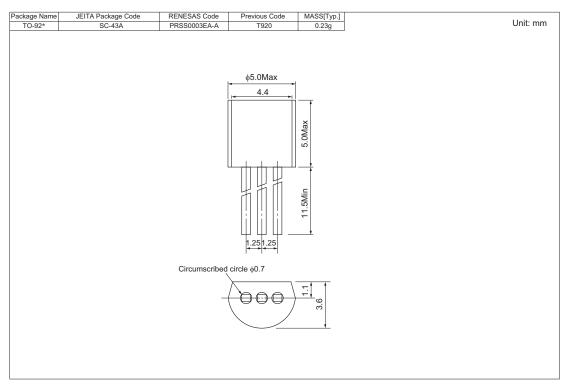






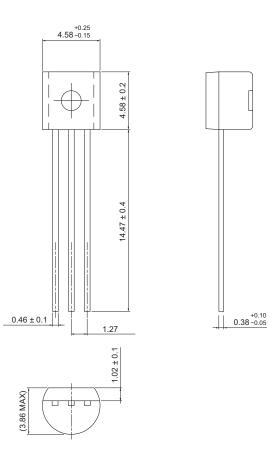


## Package dimensions



JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-43A	PRSS0003DJ-A	TO-92	0.23

Unit: mm





## **Ordering Information**

Orderable Part Number	Package	Packing Note	Quantity	Remark
CR05AM-16A#B00	TO-92*	Plastic Bag	500 pcs.	Straight type
CR05AM-16A#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, Halogen-free

Note : Please confirm the specification about the shipping in detail.



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